# FPT 2018

*IEEE International Conference on Field-Programmable Technology*

**Naha, Okinawa, Japan, 11-15 Dec, 2018**

**http://www.icfpt.org**

## 2nd Call for Papers

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### Important Dates (all 23:59, AoE):

| | | |
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| Paper submission | July | 9, 2018 |
| Author notification | September | 3, 2018 |
| Camera-ready due | October | 1, 2018 |
| Workshops, tutorials | December | 11,15, 2018 |
| Conference dates | December | 12-14, 2018 |

FPT'18
Naha

**FPT conference** is the premier conference in the Asia-Pacific region on field-programmable technologies including reconfigurable computing devices and systems containing such components. Field-programmable devices promise the flexibility of software with the performance of hardware. The development and application of field-programmable technology have become important topics of research and development. Field-programmable technology is widely applied, in high-performance computing systems, embedded and low-power control instruments, mobile communications, rapid prototyping and product emulation, among other areas.

Submissions are solicited on new research results and detailed tutorial expositions related to field-programmable technologies, including but not limited to:

### Tools and Design Techniques
for field-programmable technology including placement, routing, synthesis, verification, debugging, runtime support, technology mapping, partitioning, parallelization, timing optimization, design and run-time environments, high-level synthesis (HLS) compilers, languages and modeling techniques, provably-correct development, intellectual property core-based design, domain-specific development, hardware/software co-design.

### Architectures
for field-programmable technology including field-programmable gate arrays, complex programmable logic devices, coarse-grained reconfigurable arrays, field-programmable interconnect, field-programmable analogue arrays, field-programmable arithmetic arrays, memory architectures, interface technologies, low-power techniques, adaptive devices, reconfigurable computing systems, high-performance reconfigurable systems, evolvable hardware and adaptive computing, fault tolerance and avoidance.

### Device technology
for field-programmable logic including programmable memories such as non-volatile, dynamic and static memory cells and arrays, interconnect devices, circuits and switches, and emerging VLSI device technologies.

### Applications
of field-programmable technology including accelerators for biomedical/scientific/ neuro-morphic computing and machine learning, network processors, real-time systems, rapid prototyping, hardware emulation, digital signal processing, interactive multimedia, machine vision, computer graphics, cryptography, robotics, manufacturing systems, embedded applications, evolvable and biologically-inspired hardware.

### Education
for field-programmable technology including courses, teaching and training experience, experiment equipment, design and applications.

### Submission Guidelines:
The program committee solicits papers describing original research in field-programmable technology, including, but not limited to, the areas of interest indicated above. Papers should be submitted electronically in PDF format, following the IEEE style. Full papers should not exceed 8 pages in length, while posters should not exceed 4 pages in length. Manuscripts must not identify authors or their affiliations for double-blined review. Please use online system for submission.

### Call for workshops/tutorials:
We call for workhops and tutorials which will be held in advance to the conference.

**Further information is available at http://www.icfpt.org/**